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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/034,751	12/27/2001	Ren Egawa	01-MV-0111 (STM101-01111)	1889
7590 06/27/2006 Lisa K. Jorgenson STMicroelectronics, Inc. 1310 Electronics Drive Carrollton, TX 75006			EXAMINER TEKLE, DANIEL T	
			ART UNIT 2633	PAPER NUMBER

DATE MAILED: 06/27/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/034,751	EGAWA ET AL.	
	Examiner	Art Unit	
	Daniel Tekle	2633	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 27 December 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-21 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-21 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 27 December 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-8, 10-17 and 19-20 are rejected under 35 U.S.C. 102(b) as being anticipated by **Werner (US 6151074)**

Regarding claim 1-4, The claim drawn to an apparatus for displaying a digital still image file using a Moving Picture Expert Group (MPEG) standard comprising; a controller capable of dividing the digital still image file into a plurality of sub-picture files and capable of constructing an MPEG video stream from the plurality of sub-picture files.

An MPEG processor capable of decoding the MPEG video stream to generate a plurality of decoded sub-pictures and scaling down the plurality of decoded sub-picture to a plurality of reduced size decode sub-picture; further capable of storing the plurality of reduced size decoded sub-pictures in a display buffer; further capable of displaying contents of the display buffer only after the MPEG video stream is decoded and further capable of freezing display of display buffer contents until a second MPEG video completely decoded.

Werner art teaches an apparatus for displaying a digital still image file using a Moving Picture Expert Group (MPEG) standard comprising; a controller capable of

dividing the digital still image file into a plurality of sub-picture files and capable of constructing an MPEG video stream from the plurality of sub-picture files (**column 5 line 27-37 and line 55-68 respectively**).

An MPEG processor capable of decoding the MPEG video stream to generate a plurality of decoded sub-pictures and scaling down the plurality of decoded sub-picture to a plurality of reduced size decode sub-picture (**column 5 line 55-68**); further capable of storing the plurality of reduced size decoded sub-pictures in a display buffer (**column 3 line 61-67 and column 4 1-7**); further capable of displaying contents of the display buffer only after the MPEG video stream is decoded and further capable of freezing display of display buffer contents until a second MPEG video completely decoded (**column 3 line 61-67 and column 4 1-7**).

Regarding claim 5-8, The apparatus as set forth claim1 further including decoded memory that stores the decoded sub-pictures; a controller is further capable of determining a size for each of the plurality of sub-picture files and by calculating a quantity of 16 x 16 pixel macro blocks that is less than a maximum quantity of macro blocks that the MPEG processor can accept and decode and also capable of determining that the size of each of the plurality of sub-picture files does not exceed a size of the display buffer.

Werner art teaches The apparatus as set forth claim1 further including decoded memory that stores the decoded sub-pictures (**column 5 line 8-26**); a controller is further capable of determining a size for each of the plurality of sub-picture files and by calculating a quantity of 16 x 16 pixel macro blocks that is less than a maximum quantity

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of macro blocks that the MPEG processor can accept and decode and also capable of determining that the size of each of the plurality of sub-picture files does not exceed a size of the display buffer (**column 5 line 55-68 and column 6 line 1-2**).

Regarding claim 10-14, The claims drawn to A digital video player capable of displaying a digital still image from a digital data storage medium comprising a controller capable of dividing the digital still image file into a plurality of sub-picture files and capable of constructing an MPEG video stream from the plurality of sub-picture files.

An MPEG processor capable of decoding the MPEG video stream to generate a plurality of decoded sub-pictures and scaling down the plurality of decoded sub-picture to a plurality of reduced size decode sub-picture; a memory for storing the plurality of decoded sub-picture files; MPEG processor further capable of storing the plurality of reduced size decoded sub-pictures in a display buffer; MPEG processor further capable of displaying contents of the display buffer only after the MPEG video stream is decoded and further capable of freezing display of display buffer contents until a second MPEG video completely decoded.

Werner art teaches A digital video player capable of displaying a digital still image from a digital data storage medium (**column 3 line 37-39**) comprising a controller capable of dividing the digital still image file into a plurality of sub-picture files and capable of constructing an MPEG video stream from the plurality of sub-picture files (**column 5 line 27-37 and line 55-68 respectively**).

An MPEG processor capable of decoding the MPEG video stream to generate a plurality of decoded sub-pictures and scaling down the plurality of decoded sub-picture

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to a plurality of reduced size decode sub-picture **5 (line 27-37 and line 55-68 respectively)**; a memory for storing the plurality of decoded sub-picture files (**column 5 line 8-26**); MPEG processor further capable of storing the plurality of reduced size decoded sub-pictures in a display buffer (**column 3 line 61-67 and column 4 1-7**); MPEG processor further capable of displaying contents of the display buffer only after the MPEG video stream is decoded and further capable of freezing display of display buffer contents until a second MPEG video completely decoded (**column 3 line 61-67 and column 4 1-7**).

Regarding claims 15-17, the claim drawn to an apparatus as set forth in claims 10 a controller capable of determining a size for each of the plurality of sub-picture files; a controller is further capable of determining a size for each of the plurality of sub-picture files and by calculating a quantity of 16 x 16 pixel macro blocks that is less than a maximum quantity of macro blocks that the MPEG processor can accept and decode and further capable of determining that the size of each of the plurality of sub-picture files does not exceed a size of the display buffer.

Werner art teaches an apparatus as set forth in claims 10 a controller capable of determining a size for each of the plurality of sub-picture files (column 5 line 55-68 column 6 line 1-2 respectively); a controller is further capable of determining a size for each of the plurality of sub-picture files and by calculating a quantity of 16 x 16 pixel macro blocks that is less than a maximum quantity of macro blocks that the MPEG processor can accept and decode and further capable of determining that the size of

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each of the plurality of sub-picture files does not exceed a size of the display buffer

(column 5 line 55-68 column 6 line 1-2 respectively).

Regarding claim 19-20, The claim drawing to a method for displaying a digital still image file from digital video player comprising; dividing the digital still image file into a plurality of sub-picture files; constructing an MPEG video stream file from the plurality of sub-picture files; decoding the MPEG video stream file to generate a decoded MPEG video stream file; scaling the decoded MPEG video stream file to a reduced size video stream file; and transmitting the reduced size video stream file to a display; further the step of determining a size for the display prior to scaling the decoded MPEG video stream file.

Werner art teaches a method for displaying a digital still image file from digital video player comprising; dividing the digital still image file into a plurality of sub-picture files; constructing an MPEG video stream file from the plurality of sub-picture files; decoding the MPEG video stream file to generate a decoded MPEG video stream file; scaling the decoded MPEG video stream file to a reduced size video stream file; and transmitting the reduced size video stream file to a display; further the step of determining a size for the display prior to scaling the decoded MPEG video stream file **(column 5 line 55-68 and column 6 line 1-2 respectively).**

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claim 9, 18 and 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over **Werner** as applied to claim 1-8, 10-17 and 19-20 above, and further in view of **Demos (US 6728317)**.

Regarding claims 9, 18 and 21, claims are drawn an apparatus and method depends in claims 7, 16 and 21 respectively; where each of sub-picture files can be scaled down by overlapping a current sub-picture row of macro blocks with a last row of macro blocks from a subsequent sub-picture file.

See the teachings of **Werner** above. **Werner** does not teach a sub-picture files can be scaled down by **overlapping a current sub-picture row of macro blocks with a last row of macro blocks** from a subsequent sub-picture file. However, **Domes** teaches the enhancement of edges or borders of a frame (**column 14 line 16-25**) and the technique of overlapped block motion compensation in MPEG 4 (**column 43 line 24-63**). One ordinary skill in the art at the time of the invention was made would have motivated to use the teaching of **Domes** incorporated the teaching of **Werner** because it is know in the art to constrict image or still picture using block or macroblocks by pixel replication, cutting edge, eliminating, or overlapping to accomplish an acceptable output, as evidenced from the teachings of **Domes** and **Werner**.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel Tekle whose telephone number is 571-270-1117. The examiner can normally be reached on 7:30am to 5:00pm M-R and 7:30-4:00 Every other F..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Shanon Foley can be reached on 571-272-0898. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



Daniel Tekle
Patent Examiner



Shanon Foley
Patent Examiner Supervisory